



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Eidson et al.

Art Unit : 2183

Serial No. : 09/994,483

Examiner : Kevin P. Rizzuto

Filed : November 26, 2001

Title : SYSTEM-ON-CHIP BREAKPOINT SYNCHRONIZATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450


PETITION FOR TWO-MONTH EXTENSION OF TIME

Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated August 4, 2005, be extended for two months to and including January 4, 2006.

Enclosed is a check for \$450 for the required fee. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: January 4, 2006

  
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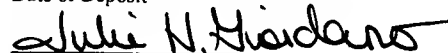
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

January 4, 2006

Date of Deposit

  
\_\_\_\_\_  
Signature

Julie H. Giordano

Typed or Printed Name of Person Signing Certificate